

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	5493	(circuit with design\$4) and ((port adj connectivity adj interface) or PCI)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/02 10:57
L7	12	(6 and load adj2 vector\$2) and (array with vector\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/02 10:55
L8	1	(circuit with design\$4) and ((port adj connectivity adj interface))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/02 10:57
L9	1	((port adj connectivity adj interface))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/02 13:46

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L10	17	((port with connectivity adj interface))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 10:58
L11	264	(hierarchic\$4 with data with structure) and (simulat\$4 with circuit\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 13:47
L12	46	11 and partition\$4 and stor\$4 and distribut\$4 and simulat\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 13:51
L13	16	12 and event and (group with circuit)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 13:51

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L14	13926	((703/13-22) or (716/1-21)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/11/02 16:30
L15	203	L14 and (simulat\$4 with circuit) and (partition\$4 with circuit) and (group\$3 with circuit\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 16:30
L16	7	L15 and (first with branch\$2) and (second with branch\$2)	US-PGPU B; USPAT; USOCR	OR	ON	2006/11/02 16:30
L17	2	L16 and monitor\$4	US-PGPU B; USPAT; USOCR	OR	ON	2006/11/02 16:30
L18	15	L15 and isomorp\$4	US-PGPU B; USPAT; USOCR	OR	ON	2006/11/02 16:30
L19	13926	((703/13-22) or (716/1-21)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/11/02 16:30

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L20	71	isomorph\$4 with (behavior or event\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 16:30
L21	34	L19 and L20	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 16:30
L22	4	L21 and (simulat\$4 with circuit) and (partition\$4 with circuit) and (subcircuit\$2 or subdivid\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 16:30
L23	4	L22 and (data with structure\$2) and stor\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 16:30

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L24	2	L23 and (input\$4 with vector\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/11/02 16:30
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